

Applicant: Jong-Soo Choi, Byung-Man Kim, Joung-Rhang Lee and Chang-Yong Park  
Filing Date: December 29, 2003 Art Unit: not yet assigned  
Serial No.: not yet assigned Examiner: not yet assigned  
Title: PRINTED CIRCUIT BOARD WHICH CAN BE CONNECTED WITH PIN CONNECTOR  
AND METHOD OF MANUFACTURING THE PRINTED CIRCUIT BOARD

**INFORMATION DISCLOSURE CITATION**  
**FORM PTO-1449 (Modified)**

**FOREIGN PATENT DOCUMENTS**

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Publication</u> <u>Date</u>	<u>Country</u>	<u>Name</u>
_____	_____	JP2001-23738	1/10/2001	Japan	Kato and Yahiro

**OTHER DOCUMENTS**

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.)</u>
_____	_____	English language of Abstract for Japanese patent publication No. JP2001-23738, published on 1/10/2001.

Examiner: \_\_\_\_\_

Date Considered: \_\_\_\_\_